

# **TOUCH PULSE DIALER WITH REDIAL**

### GENERAL DESCRIPTION

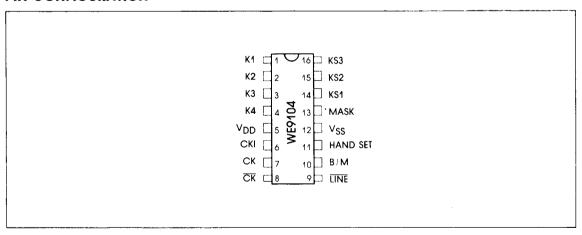
The WE9104 is a monolithic CMOS integrated circuit which converts keyboard inputs into pulse signal outputs simulating a rotary telephone dial. It is designed to operate directly from the telephone line and can be interfaced properly to meet telephone specifications in system utilizing loop disconnect signaling.

#### **FEATURES**

- 2.0V to 5.5V supply voltage.
- Low power standby mode for redial.
- Inexpensive RC oscillator used as frequency reference.
- Redial with either a \* or # input.
- Inter digital pause (9104: 800ms.)

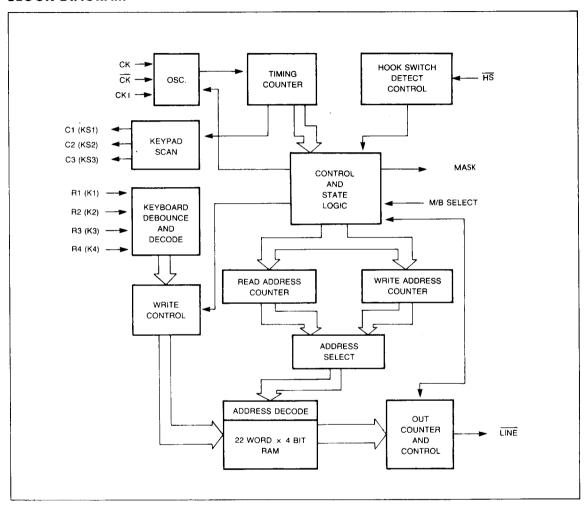
- Pin selectable Break/Make.
- Provision for rapid Testing.
- Dialer reset for line power breaks 200mS.
- 22 digit capacity for redial.
- Inexpensive 4x3 matrix single contact keypad.

#### PIN CONFIGURATION





## **BLOCK DIAGRAM**



# **ABSOLUTE MAXIMUM RATINGS**

CHARACTERISTICS	RATING	UNIT
DC Supply Voltage	5.5	V
Operating Temperature	-20 to +60	°C
Storage Temperature	-55 to +120	°C

# **ELECTRICAL CHARACTERISTICS**

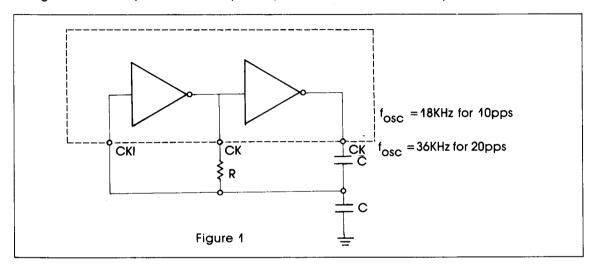
 $V_{DD}$  =5V,  $V_{SS}$  =0V,  $T_A$  = 25°C, unless otherwise specified.

PARAMETER	CONDITION	LIMIT			
		MIN	TYPE	MAX	UNITS
Logic 0:B:M		-0.3	0.0	1.5	٧
All Other Inputs		-0.3	0.0	1.5	٧
Logic 1:B:M		V <sub>DD</sub> -1.5	v <sub>DD</sub>	V <sub>DD</sub> +0.3V	٧
All Other Inputs		V <sub>DD</sub> - 1.5	V <sub>DD</sub>	V <sub>DD</sub> +0.3	٧
Keypad Inputs	Vin=V <sub>SS</sub>	40	60	150	μΑ
Sink Current:B:M	Vin=V <sub>DD</sub>	0.0	0.1	1.0	μΑ
Key Depression Period	Vin=V <sub>SS</sub> or V <sub>DD</sub>	30.0	40.0		mS
Mask Sink Current Drive Current	$V_{DD} = 2.0V$ $V_{O} = 1V$ $V_{O} = V_{DD} - 1V$	2	10.0 6.0		mA
Line Sink Current Leakage Current	$V_{DD} = 2.0V$ $V_{O} = 1V$	2	7.0		mA
	V <sub>O</sub> =2.0V		0.0	1.0	μA
Clock Frequency	$V_{DD} = 2.0V$ $V_{DD} = 3.9V$ $V_{DD} = 5V$	14.3 17.2 17.8	17.2 18.0 18.2	18.1 18.6 19.5	KHz KHz KHz
Stand-by Current	V <sub>DD</sub> =5V		0.5	4	μΑ
Operating Current	V <sub>DD</sub> =5V, Not Key in		8.0	40.0	μА
	V <sub>DD</sub> =5V, Key in		30.0	200.0	μΑ

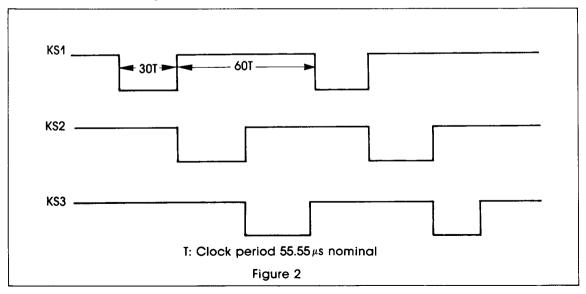


### FUNCTION DESCRIPTION

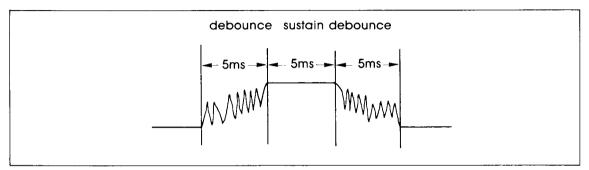
Clock oscillator-The clock oscillator consists of two inverters, the frequency of oscillation being controlled by external components connected to these three pins.



Keypad Scan, Input and Debounce-The keypad scan is at the rate of 200Hz, its waveform is shown in Figure 2.



The keypad input are normally held at high by means of on-chip pull up resistors. When a key is depressed, the dialers identifies the key by examining all four keypad inputs To prevent mutiple reading of digits due to contact bounce, the dialer incorporates circuitry which delay the reading of data until certain conditions are met. This operates as follows:

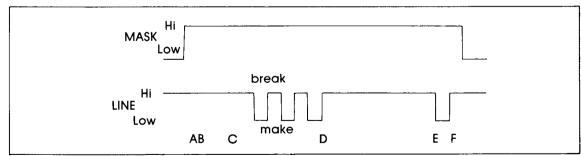


Hook Switch Detect Control-External circuitry connected to Handset Pin is used to indicate whether the telephone handset is on-hook or off-hook, these two states being represented by Logic Hi and Logic Low respectively.

Line Output-The output stage consists of an N-channel open-drain device sinking current to  $V_{SS}$ . During a dial pulse break period the out - put device is switched on and during the

make period and IDP the output device is switched off. The output drives an external bipolar transistor that sequentially opens the telephone loop a number of times equal to the input digit selected.

Mask Out-This pin is used to control the muting of the telephone network during out dialing. (See Figure 3)



Time Intrval

 $T_{AB} = 5-10$ ms after end of bounce,  $T_{BC} = 800$ ms

 $T_{CD} = n \times 100$ ms where n=digit dialed,  $T_{DE} = 800$ ms

 $T_{EF} = n \times 100$ ms where n=digit dialed.

Figure 3

REDIAL-After normal dialing, ON-HOOK OFF-HOOK or#, then last number dialed can be redialed out.



# TYPICAL APPLICATION CIRCUIT (B/M=67:33)

