- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

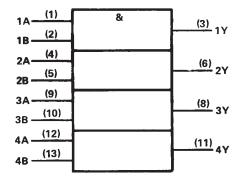
These devices contain four independent 2-input AND gates.

The SN5408, SN54LS08, and SN54S08 are characterized for operation over the full military temperature range of $-55\,^{\circ}\text{C}$ to $125\,^{\circ}\text{C}$. The SN7408, SN74LS08 and SN74S08 are characterized for operation from 0° to 70°C.

FUNCTION TABLE (each gate)

INP	UTS	OUTPUT
Α	В	Υ
Н	Н	Н
L	X	L
×	L	Ļ

logic symbol†



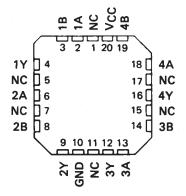
[†]This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

SN5408, SN54LS08, SN54S08 . . . J OR W PACKAGE SN7408 . . . J OR N PACKAGE SN74LS08, SN74S08 . . . D, J OR N PACKAGE (TOP VIEW)

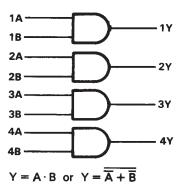
	_		
1A 🗆	1	U 14	□ vcc
18 🗆	2	13] 4B
1Y 🗆	3	12	D 4A
2A 🗀	4	11	□ 4Y
28 🗆	5	10] 3B
2Y 🗀	6	9] 3A
GND [7	8] 3Y

SN54LS08, SN54S08 . . . FK PACKAGE (TOP VIEW)

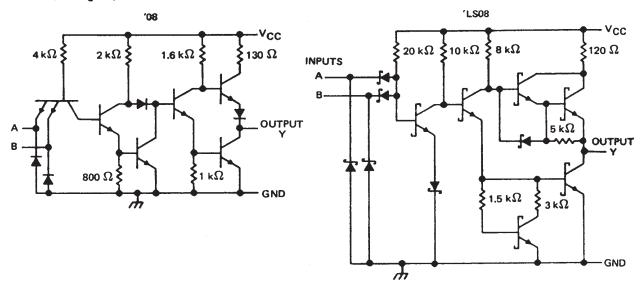


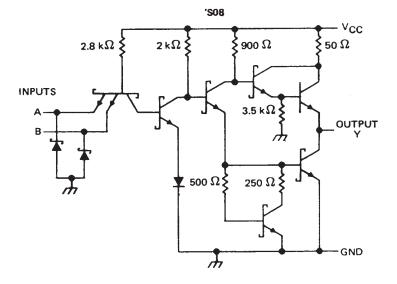
NC-No internal connection

logic diagram (positive logic)



schematics (each gate)





Resistor values are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)		7 V
Input voltage: '08, 'S08		5.5 V
Operating free-air temperature range:	SN54'	55°C to 125°C
	SN74'	0°C to 70°C
Storage temperature range		65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.



recommended operating conditions

		SN5406	3	SN7408			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC Supply voltage	4.5	5	5.5	4.75	5	5.25	٧
VIH High-level input voltage	2			2			٧
V _{IL} Low-level input voltage			0.8			8.0	٧
IOH High-level output current			- 0.8			- 0.8	mA
IOL Low-level output current			16			16	mA
TA Operating free-air temperature	- 55		125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

				SN540	3		SN740	В	UNIT
PARAMETER		TEST CONDITIONS T	MIN	TYP\$	MAX	MIN	TYP‡	MAX	UNIT
VIK	V _{CC} = MIN,	I ₁ = - 12 mA			<i>-</i> 1.5			- 1.5	V
Voн	V _{CC} = MIN,	V _{1H} = 2 V, I _{OH} = -0.8 mA	2.4	3.4		2.4	3.4		.V
VOL	V _{CC} = MIN,	V _{IL} = 0.8 V, I _{OL} = 16 mA		0.2	0.4		0.2	0.4	V
l _į	V _{CC} = MAX,	V _I = 5.5 V			1			1	mA
ин	V _{CC} = MAX,	V _I = 2.4 V			40			40	μΑ
l _I L	V _{CC} = MAX,	V ₁ = 0.4 V			- 1.6			- 1.6	mA
IOS§	V _{CC} = MAX		- 20		- 55	- 18		- 55	mA
¹ ССН	V _{CC} = MAX,	V ₁ = 4.5 V		11	21		11	21	mA
¹ CCL	V _{CC} = MAX,	V ₁ = 0 V		20	33		20	33	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDIT	TEST CONDITIONS				
tPLH						17.5	27	ns
tPHL	A or B	Y	$R_L = 400 \Omega$,	CL = 15 pF		12	19	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$. § Not more than one output should be shorted at a time.

recommended operating conditions

			SN54LS	08		UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNII
vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	٧
VIН	High-level input voltage	2			2			٧
VIL	Low-level input voltage			0.7			0.8	٧
ЮН	High-level output current			- 0.4			- 0.4	mA
loL	Low-level output current			4			8	mA
TA	Operating free-air temperature	- 55		125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

					SN54LS	08		SN74LS	08	UNIT
PARAMETER		TEST CONDIT	TONS I	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	VCC = MIN,	I ₁ = - 18 mA				- 1.5			- 1.5	٧
Voн	V _{CC} = MIN,	V _{IH} = 2 V,	I _{OH} = - 0.4 mA	2.5	3.4		2.7	3.4		٧
.,	V _{CC} = MIN,	VIL = MAX,	I _{OL} = 4 mA		0.25	0.4		0.25	0.4	V
VOL	VCC = MIN,	VIL = MAX,	IOL = 8 mA					0.35	0.5	v
11	V _{CC} = MAX,	V ₁ = 7 V				0.1			0.1	mA
ΊΗ	V _{CC} = MAX,	V _I = 2.7 V				20			20	μΑ
1 ₁ L	V _{CC} = MAX,	V ₁ = 0.4 V				- 0.4		-	- 0.4	mA
los§	V _{CC} = MAX			- 20		100	- 20		- 100	mA
¹ ссн	V _{CC} = MAX,	V ₁ = 4.5 V			2.4	4.8		2.4	4.8	mA
ICCL	V _{CC} = MAX,	V ₁ = 0 V			4.4	8.8		4.4	8.8	mA

switching characteristics, VCC = 5 V, TA = 25°C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN	TYP	MAX	UNIT	
tPLH	A or B	V	$R_1 = 2 k\Omega$,	C 15 oF		8	15	ns
^t PHL	A OF B	'	11[- 2 K14,	C _L = 15 pF		10	20	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. ‡ All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

recommended operating conditions

			SN5	150	8		SN74S0	8	UNIT
		MIN	NO	M	MAX	MIN	NOM	MAX	UNII
Vcc	Supply voltage	4.5		5	5.5	4.75	5	5.25	٧
VIH	High-level input voltage	2				2			V
VIL	Low-level input voltage				8.0		_	0.8	V
ЮН	High-level output current				- 1			– 1	mA
loL	Low-level output current				20			20	mA
TA	Operating free-air temperature	- 55			125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

					SN54S0	8		UNIT		
PARAMETER		TEST CONDIT	TIONS T	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	V _{CC} = MIN,	I ₁ = -18 mA				-1.2		_	-1.2	٧
V _{OH}	V _{CC} = MIN,	V _{IH} = 2 V,	I _{OH} = -1 mA	2.5	3.4		2.7	3.4		٧
V _{OL}	V _{CC} = MIN,	V _{1L} = 0.8 V	I _{OL} = 20 mA			0.5			0.5	٧
11	V _{CC} = MAX,	V _I ≈ 5.5 V				1			1	mA
Iн	V _{CC} = MAX,	V _I = 2.7 V				50			50	μΑ
IL	V _{CC} = MAX,	V ₁ = 0.5 V				-2			-2	mA
1 _{OS} §	V _{CC} = MAX			-40		-100	-40		-100	mA
Іссн	V _{CC} = MAX,	V _I = 4.5 V			18	32		18	32	mA
ICCL	V _{CC} = MAX,	V _I = 0 V			32	57		32	57	mA

¹ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _{PLH}			$R_1 = 280 \Omega$, $C_L = 15 pF$		4.5	7	ns
tPHL.	4 . 5	V	NC - 200 32, CC - 13 pr		5	7.5	ns
^t PLH	A or B	Y	R ₁ = 280 Ω, C ₁ = 50 pF		6		ns
tPHL			R _L = 280 Ω, C _L = 50 ρF		7,5		ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$. § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

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dsp.ti.com	Broadband	www.ti.com/broadband
interface.ti.com	Digital Control	www.ti.com/digitalcontrol
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power.ti.com	Optical Networking	www.ti.com/opticalnetwork
microcontroller.ti.com	Security	www.ti.com/security
www.ti.com/lpw	Telephony	www.ti.com/telephony
	Video & Imaging	www.ti.com/video
	Wireless	www.ti.com/wireless
	dataconverter.ti.com dsp.ti.com interface.ti.com logic.ti.com power.ti.com microcontroller.ti.com	amplifier.ti.com dataconverter.ti.com dsp.ti.com dsp.ti.com interface.ti.com logic.ti.com power.ti.com microcontroller.ti.com www.ti.com/lpw Audio Audio Audio Audio Automotive Broadband Digital Control Military Optical Networking Security Telephony Video & Imaging

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interface.ti.com	Digital Control	www.ti.com/digitalcontrol
logic.ti.com	Military	www.ti.com/military
power.ti.com	Optical Networking	www.ti.com/opticalnetwork
microcontroller.ti.com	Security	www.ti.com/security
www.ti.com/lpw	Telephony	www.ti.com/telephony
	Video & Imaging	www.ti.com/video
	Wireless	www.ti.com/wireless
	dataconverter.ti.com dsp.ti.com interface.ti.com logic.ti.com power.ti.com microcontroller.ti.com	amplifier.ti.com dataconverter.ti.com dsp.ti.com dsp.ti.com interface.ti.com logic.ti.com power.ti.com microcontroller.ti.com www.ti.com/lpw Audio Audio Audio Audio Automotive Broadband Digital Control Military Optical Networking Security Telephony Video & Imaging







PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
JM38510/08003BCA	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
JM38510/08003BDA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
JM38510/08003BDA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
JM38510/31004B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
JM38510/31004B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
JM38510/31004BCA	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
JM38510/31004BCA	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
JM38510/31004BDA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
JM38510/31004BDA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
JM38510/31004SCA	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
JM38510/31004SCA	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
JM38510/31004SDA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
JM38510/31004SDA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
SN54LS08J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SN54LS08J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SN54S08J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SN54S08J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SN7408N	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
SN7408N	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
SN7408N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
SN7408N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
SN74LS08D	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08D	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DBR	ACTIVE	SSOP	DB	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DBR	ACTIVE	SSOP	DB	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DBRE4	ACTIVE	SSOP	DB	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DBRE4	ACTIVE	SSOP	DB	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS &	CU NIPDAU	Level-1-260C-UNLIM





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23-Apr-2007

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
						no Sb/Br)		
SN74LS08DRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08DRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI
SN74LS08J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI
SN74LS08N	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74LS08N	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74LS08N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
SN74LS08N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
SN74LS08NE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74LS08NE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74LS08NSR	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08NSR	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08NSRE4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08NSRE4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08NSRG4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS08NSRG4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08D	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08D	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08DE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08DE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08DG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08DG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIN
SN74S08DR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIN
SN74S08DR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIN
SN74S08DRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIN
SN74S08DRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIN





.com 23-Apr-2007

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
SN74S08DRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08DRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI
SN74S08J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI
SN74S08N	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74S08N	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74S08N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
SN74S08N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
SN74S08NE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74S08NE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74S08NSR	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08NSR	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08NSRE4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74S08NSRE4	ACTIVE	so	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SNJ54LS08FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
SNJ54LS08FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
SNJ54LS08J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SNJ54LS08J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SNJ54LS08W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
SNJ54LS08W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
SNJ54S08FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
SNJ54S08FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type
SNJ54S08J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SNJ54S08J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SNJ54S08W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
SNJ54S08W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.



PACKAGE OPTION ADDENDUM

23-Apr-2007

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

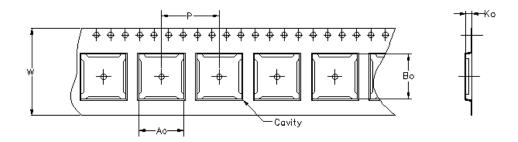
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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Carrier tape design is defined largely by the component lentgh, width, and thickness.

Ao =	Dimension	designed	to	accommodate	the	component	width.				
Bo =	Dímension	designed	to	accommodate	the	component	length.				
Ko =	Dímension	designed	to	accommodate	the	component	thickness.				
W =	W = Overall width of the carrier tape.										
P = 1	Pitch betwe	en succes	ssiv	e cavity center	ຮ.						



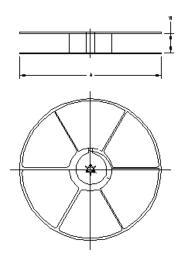
TAPE AND REEL INFORMATION



PACKAGE MATERIALS INFORMATION

30-Apr-2007

Device	Package	Pins	Site	Reel Diameter (mm)	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74LS08DBR	DB	14	MLA	330	16	8.2	6.6	2.5	12	16	Q1
SN74LS08DR	D	14	MLA	330	16	6.5	9.0	2.1	8	16	Q1
SN74LS08NSR	NS	14	MLA	330	16	8.2	10.5	2.5	12	16	Q1
SN74S08DR	D	14	MLA	330	16	6.5	9.0	2.1	8	16	Q1
SN74S08NSR	NS	14	MLA	330	16	8.2	10.5	2.5	12	16	Q1



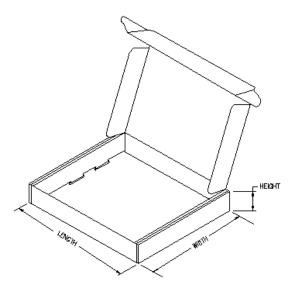
TAPE AND REEL BOX INFORMATION

Device	Package	Pins	Site	Length (mm)	Width (mm)	Height (mm)
SN74LS08DBR	DB	14	MLA	333.2	333.2	28.58
SN74LS08DR	D	14	MLA	333.2	333.2	28.58
SN74LS08NSR	NS	14	MLA	333.2	333.2	28.58
SN74S08DR	D	14	MLA	333.2	333.2	28.58
SN74S08NSR	NS	14	MLA	333.2	333.2	28.58





30-Apr-2007



14 LEADS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

W (R-GDFP-F14)

CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP1-F14 and JEDEC MO-092AB



FK (S-CQCC-N**)

28 TERMINAL SHOWN

LEADLESS CERAMIC CHIP CARRIER



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. The terminals are gold plated.
- E. Falls within JEDEC MS-004



N (R-PDIP-T**)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



D (R-PDSO-G14)

PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AB.



MECHANICAL DATA

NS (R-PDSO-G**)

14-PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



DB (R-PDSO-G**)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-150

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